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Prelab5

1. Add, Subtract, Multiply operations:
   1. ADC. Adds two 8 bit registers, adds carry
   2. ADD. Adds two 8 bit register, without carry
   3. ADIW. Adds a 6 bit constant and a 16 bit register pair
   4. FMUL. Multiples two 8 bit registers as a unsigned fraction
   5. FMULS. Multiples two 8 bit registers as a signed fraction
   6. FMULSU. Multiples two 8 bit register, one signed and one unsigned fractional number
   7. MUL. Multiples two 8 bit registers as signed numbers
   8. MULS. Multiples two 8 bit registers, as unsigned fraction
   9. MULSU. Multiples two 8 bit registers, one signed and one unsigned fractional number
   10. SBC. Subtracts two 8 bit registers and subtracts carry
   11. SBIW. Subtracts one 6 bit constant from a 16 bit register pair
   12. SUB. Subtracts two 8 bit registers without carry
   13. SUBI. Subtracts 8 bit constant from and 8 bit register without carry

2 Pseudo code for 16 bit add

1 Load operands into registers

2 Add registers containing the lower byte of the 16 bit number, carry gets set if needed

3 Add with carry the registers containing the higher byte of the 16 bit numbers

4 Store first result in $0100

5 Store second result in $0101

3 Pseudo code for 16 bit subtract

1 Load operands into registers

2 Subtract registers containing the lower byte of the 16 bit number, carry gets set if needed

3 Add with carry the registers containing the higher byte of the 16 bit numbers

4 Store first result in $0100

5 Store second result in $0101